

REMARKS

Pursuant to 37 C.F.R. § 1.111, reconsideration of the Office Action dated August 7, 2006, is respectfully requested by Applicant.

Summary

Claims 1 and 3 – 4 are currently pending.

Claim Rejections

Claims 1, 3, and 4 were rejected pursuant to 35 U.S.C. § 102(e) as being anticipated by Lin (U.S. Pat. Pub. No. 2003/0212841).

Claim 1 recites, *inter alia*, a control unit that transfers the data stored in the second buffer memory to the first buffer memory.

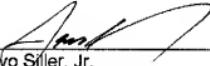
Lin fails to disclose or suggest a control unit that transfers the data stored in the second buffer memory to the first buffer memory. Lin teaches the "ports 32 and 36 are connected to a control circuit 42 through FIFOs 34 and 38, respectively" (paragraph 0020). The FIFOs are connected to a control circuit 20 and relay digital information back and forth between the control circuit and the USB ports. (paragraph 0007). There is no suggestion that the control unit transfers data stored in the second buffer memory to the first buffer memory. Accordingly, claim 1 is allowable over the cited art.

Dependent claims 3 and 4 depend from allowable claim 1, so are allowable for at least these reasons.

Conclusion

Applicants respectfully submit that all of the pending claims are in condition for allowance and seek an early allowance thereof. If for any reason the Examiner is unable to allow the application in the next Office Action and believes that a telephone interview would be helpful to resolve any remaining issues, he is respectfully requested to contact the undersigned.

Respectfully submitted,



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